HASP 2016 Student Payload Monthly Report

Payload Flight Number:				Institution:							
20016-09				University of Central Florida							
Payload Title:											
Hazardous Gases for Harsh Environments LED Sensor											
Student Leader:					Faculty Advisor:						
Michael Villar (Graduate Student)				Dr. Subith Vasu							
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Current Team Members:											
Justin Urso (Graduate Student) Akshita Parupalli (Undergraduate Student) Mechanical and Aerospace Engineering											
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Past Team Members:											
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Report Month: Report Date:											
June				6/24/2016							
June				0/24/2010							
Gantt Chart:											
May				June							
Tasks	5/1/2016	5/8/2016		5/22/2016	5/29/2016	6/5/2016			6/26/2016		
DAQ Programing (K,M)	х	Х									
Environmental Chamber Testing 2 (M,A)			Х	Х							
1st Paper (K,M,A) HASP Chassis Construction (M,A)					X X	X X	X	x	X		
HASP Electronics Readiness (M,A)					~	~	X	X	X		
Corrective Actions (M,A)		Х									
HASP ST&P Measurements (M,A)											
HASP Flight (M,A,J)	Į							-			

Team Member Activities:

Michael Villar worked on addressing all HASP PSIP review comments this month, in addition to resolving any problems slight modifications to the final design were made to the payload layout to improve functionality and is reflected in the final PSIP. All of the final structural materials for the payload chassis arrived this month as well so finalized construction is in full swing and on track with the summer schedule.

Akshita Parupalli has assisted in the final construction of the payload chassis this month as well as doing additional materials analysis with bonding agents for the PVC cell to select an adhesive that does not have any outgassing properties that would affect data collection. The decision was made go with PVC for the gas cell as it had the best properties in the temperature range that it will be submitted to.

Justin Urso's focus has been on an outside research project and was mainly available for consultation and environmental chamber testing assistance.

System Updates:

- Update: Updated Electronics layout in finalized payload chassis
 - The internal electronics layout has been optimized to reduce wire routing length. Specifically designed 12pin wire connectors are being used as a hermetically sealing method as the wires pass out of the payload chassis and onto the optics mounted outside.
- Update: Continual modifications to the Payload chassis
 - The remainder of the finalized payload chassis construction materials arrived this month and machining and construction is underway and on track for completion in the next 2 weeks. This will give ample time to conduct failure mode tests to improve the robustness of the overall system.